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Thoroughly updated, the new edition covers CAD tools, PLA's and VLSI systems. new, open-ended design problems encourage students to practice designing. About the Author Frederick J. Hill is the author of Computer Aided Logical Design with Emphasis on VLSI, 4th Edition, published by Wiley.

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Computer-aided design of digital circuits and systems is a modern design standard. However, logical properties of the current-mode gates, as well as approaches to designing current-mode digital circuits are specific and unknown to most designers. Therefore, in this paper, we propose the computer-

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Computer Aided Logical Design With Emphasis On Vlsi [PDF]

CALD: Computer-Aided Logic Design System Géry Bioul, Daniel Simonelli \*, Valeria Otaegui y Susana Heineke Facultad de Ciencias Exactas Universidad Nacional del Centro de la Provincia de Buenos Aires INCA Institute – TE: +54 2293 432466 - Paraje Arroyo Seco, (7000) Tandil, Argentina e-mail: dsimonel@exa.unicen.edu.ar

CALD: Computer-Aided Logic Design System

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information however this fiction is increasingly hard to maintain as computing devices decrease in size and systems increase in

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Electronic design automation, also referred to as electronic  
computer-aided design, is a category of software tools for designing  
electronic systems such as integrated circuits and printed circuit  
boards. The tools work together in a design flow that chip designers  
use to design and analyze entire semiconductor chips. Since a  
modern semiconductor chip can have billions of components, EDA  
tools are essential for their design. This article describes EDA  
specifically with respect to integrated circ

Tied to no particular set of computer-aided logic design tools, it  
advocates the new emphasis in VLSI design. Includes support of  
layout synthesis from description in a register transfer level

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language as well as from design capture. Contains a detailed introduction to Boolean algebra, Karnaugh maps and sequential circuits. In this edition discussion of combination logic has been extended; switching circuits updated; a comprehensive treatment of test generation for VLSI included.

Structured computer-aided digital logic design is defined and an example of it is given, the S-1 Design System. It is noted that such a system has resulted in a very large reduction in the amount of designer effort required to complete the design of a large-scale digital processing system in the first exercise of its capabilities. This basic and generally applicable advance in digital design technology may be expected to affect quite favorably all complex digital systems developments. 3 figures.

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